

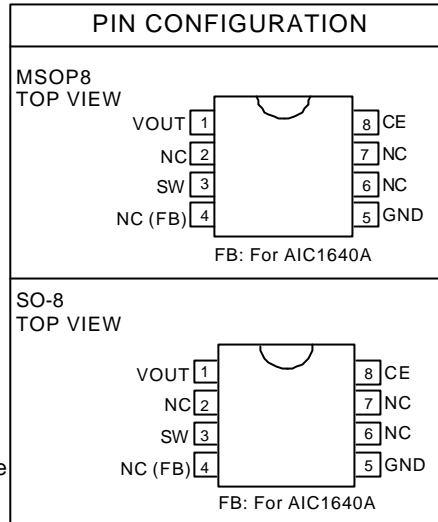


## ORDERING INFORMATION

AIC1640-XXCXXX

- PACKING TYPE
  - TR: TAPE & REEL
  - TB: TUBE
- PACKAGE TYPE
  - O: MSOP8
  - S: SMALL OUTLINE
- OUTPUT VOLTAGE
  - 27: 2.7V
  - 30: 3.0V
  - 33: 3.3V
  - 50: 5.0V
  - A: Adjustable

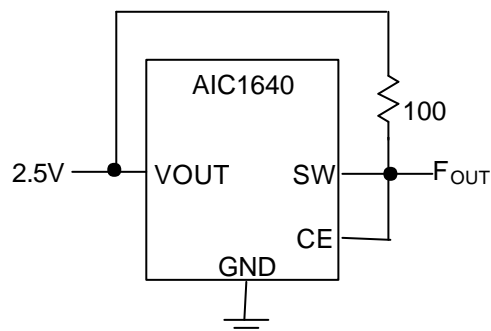
Example: AIC1640-27COTR  
 → 2.7V Version, in MSOP8 Package & Tape & Reel Packing Type



## ABSOLUTE MAXIMUM RATINGS

Supply Voltage	.....	12V
SW Pin Voltage	.....	12V
FB Pin Voltage	.....	12V
SW Pin Switch Current	.....	0.6A
Operating Temperature Range	.....	-40 °C to 85°C
Storage Temperature Range	.....	-65°C to 150 °C
Lead Temperature (Soldering 10 Sec.)	.....	260°C

## TEST CIRCUIT



Oscillator Test Circuit

**ELECTRICAL CHARACTERISTICS** ( $T_A=25^\circ\text{C}$ ,  $I_O=10\text{mA}$ , Unless otherwise specified)

PARAMETER	TEST CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Output Voltage	AIC1640-27 $V_{IN}=1.8\text{V}$	$V_{OUT}$	2.633	2.700	2.767	V
	AIC1640-30 $V_{IN}=1.8\text{V}$		2.925	3.000	3.075	
	AIC1640-33 $V_{IN}=2.0\text{V}$		3.218	3.300	3.382	
	AIC1640-50 $V_{IN}=3.0\text{V}$		4.875	5.000	5.125	
Input Voltage		$V_{IN}$			8	V
Start-Up Voltage	$I_{OUT}=1\text{mA}$ , $V_{IN}:0\rightarrow 2\text{V}$	$V_{START}$		0.8	0.9	V
Hold-on Voltage	$I_{OUT}=1\text{mA}$ , $V_{IN}:2\rightarrow 0\text{V}$	$V_{HOLD}$			0.7	V
No-Load Input Current	$I_{OUT}=0\text{mA}$	$I_{IN}$		15		$\mu\text{A}$
Supply Current	AIC1640-27	$I_{DD1}$		42		$\mu\text{A}$
	AIC1640-30			50		
	AIC1640-33			60		
	AIC1640-50			90		
	$V_{IN}=V_{OUT} \times 0.95$ Measurement of the IC input current (VOUT pin)					
Supply Current	$V_{IN}=V_{OUT} + 0.5\text{V}$ Measurement of the IC input current (VOUT pin)	$I_{DD2}$		7		$\mu\text{A}$
FB Reference Voltage		$V_{REF}$	1.21	1.24	1.27	V
SW Leakage Current	$V_{SW}=10\text{V}$ , $V_{IN}=V_{OUT} + 0.5\text{V}$				0.5	$\mu\text{A}$
SW Switch-On Resistance	AIC1640-27	$R_{ON}$		1.3		$\Omega$
	AIC1640-30			1.2		
	AIC1640-33			1.1		
	AIC1640-50			1		
	$V_{IN}=V_{SW} \times 0.95$ , $V_{SW}=0.4\text{V}$					
Oscillator Duty Cycle	$V_{IN}=V_{OUT} \times 0.95$ Measurement of the SW Pin waveform	DUTY	65	75	85	%

**ELECTRICAL CHARACTERISTICS (Continued)**

PARAMETER	TEST CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Max. Oscillator Freq.	$V_{IN}=V_{OUT} \times 0.95$ Measurement of the SW pin waveform	F <sub>OSC</sub>	80	105	130	KHz
CE "High" Voltage Level 1	$V_{OUT} \geq 1.5V$	V <sub>CEH1</sub>	$V_{OUT} - 0.4$			V
CE "Low" Voltage Level 1	$V_{OUT} \geq 1.5V$	V <sub>CEL1</sub>	0.4			V
CE "High" Voltage Level 2	$0.8V \leq V_{OUT} \leq 1.5V$	V <sub>CEH2</sub>	$V_{OUT} - 0.3$			V
CE "Low" Voltage Level 2	$0.8V \leq V_{OUT} \leq 1.5V$	V <sub>CEL2</sub>	0.15			V
CE "High" Input Current	Same as I <sub>DD1</sub> , V <sub>CE</sub> =V <sub>OUT</sub> × 0.95	I <sub>CEH</sub>	0.5			μA
CE "Low" Input Current	Same as I <sub>DD1</sub> , V <sub>CE</sub> = 0V	I <sub>CEL</sub>	-0.5			μA
Efficiency		η	85			%

**TYPICAL PERFORMANCE CHARACTERISTICS**

Capacitor (C1) : 47μF (Tantalum Type)

Diode (D1) : 1N5819 Schottky Type

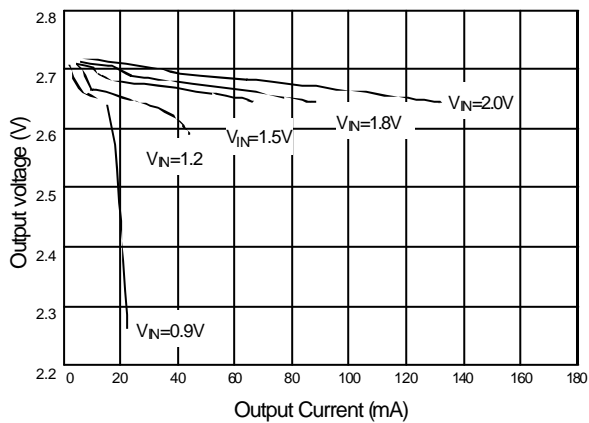


Fig. 1 AIC1640-27 Load Regulation (L=100μH CD54)

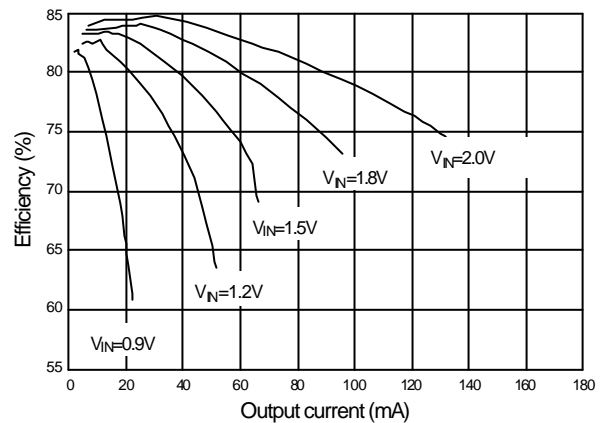


Fig. 2 AIC1640-27 Efficiency (L=100μH CD54)

■ **TYPICAL PERFORMANCE CHARACTERISTICS (Continued)**

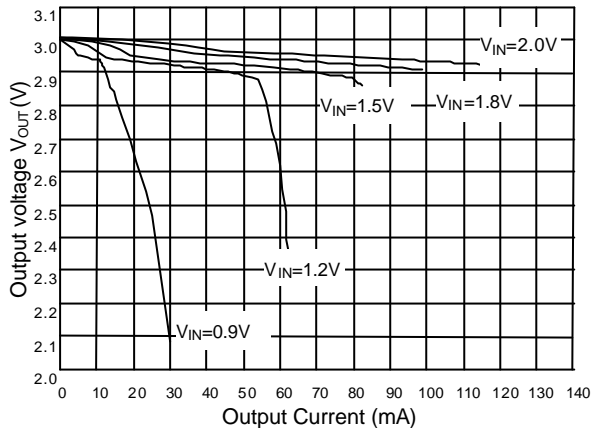


Fig. 3 AIC1640-30 Load Regulation (L=100µH CD54)

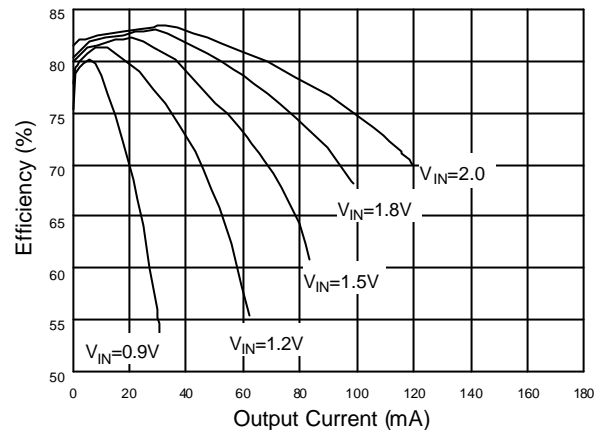


Fig. 4 AIC1640-30 Efficiency (L=100µH CD54)

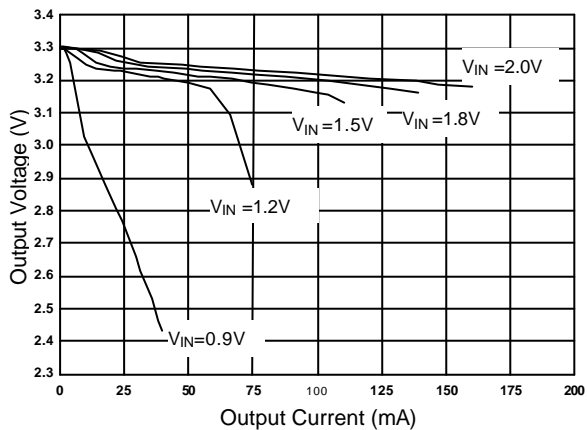


Fig. 5 AIC1640-33 Load Regulation (L=100µH CD54)

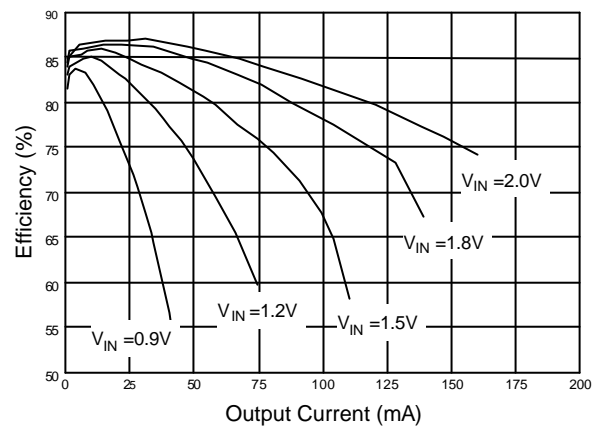


Fig. 6 AIC1640-33 Efficiency (L=100µH CD54)

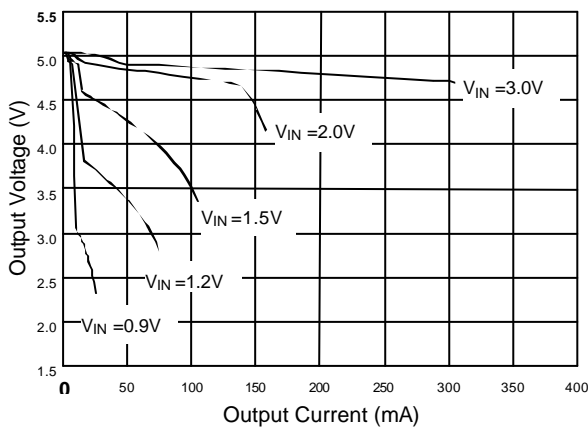


Fig. 7 AIC1640-50 Load Regulation (L=100µH CD54)

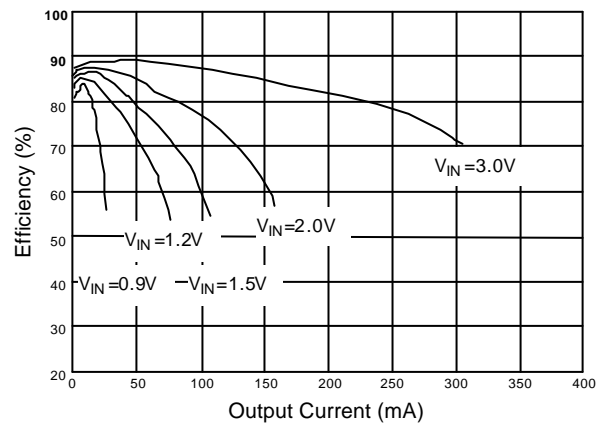


Fig. 8 AIC1640-50 Efficiency (L=100µH CD54)

**TYPICAL PERFORMANCE CHARACTERISTICS (Continued)**

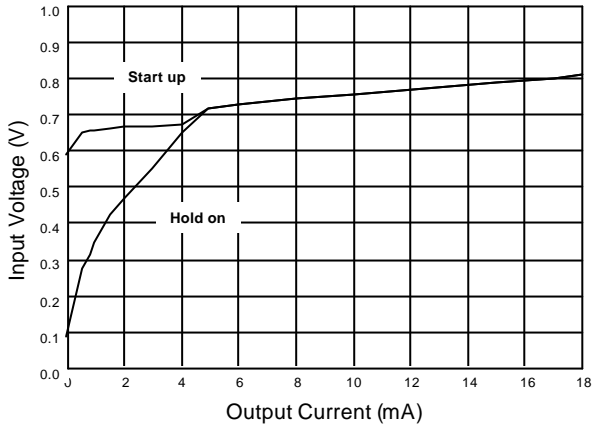


Fig. 9 AIC1640-27 Start-up & Hold-on Voltage

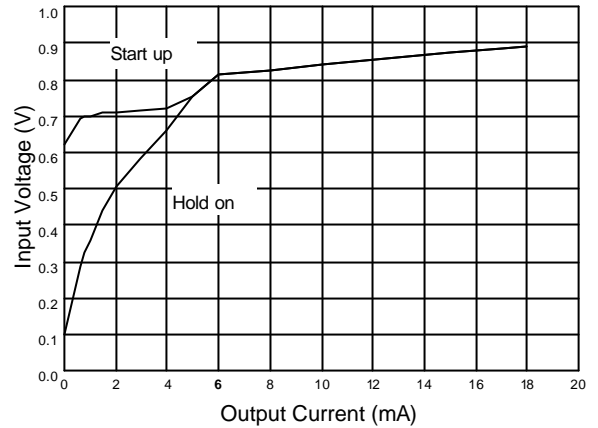


Fig. 10 AIC1640-30 Start-up & Hold-on Voltage

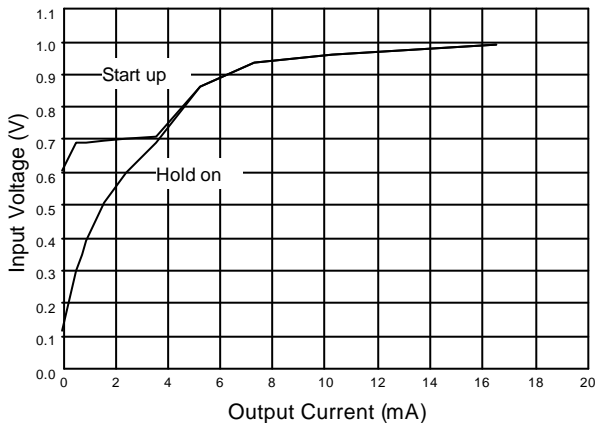


Fig. 11 AIC1640-33 Start-up & Hold-on Voltage (L=100μH)

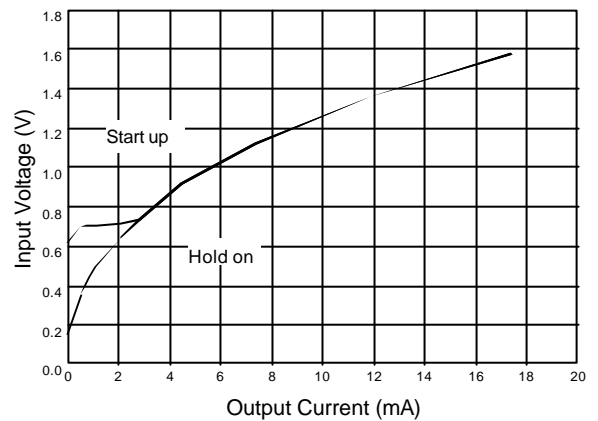


Fig. 12 AIC1640-50 Start-up & Hold-on Voltage (L=100μH)

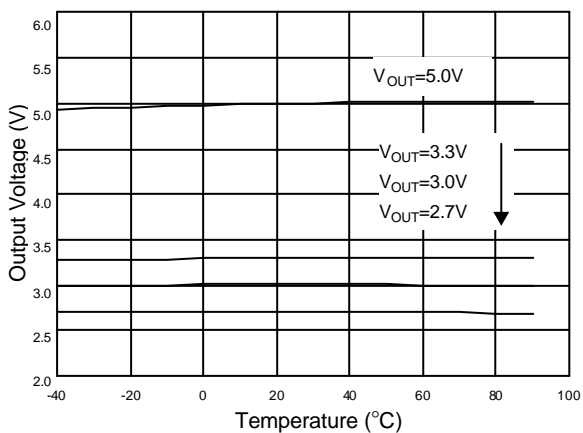


Fig. 13 AIC1640 Output Voltage vs. Temperature

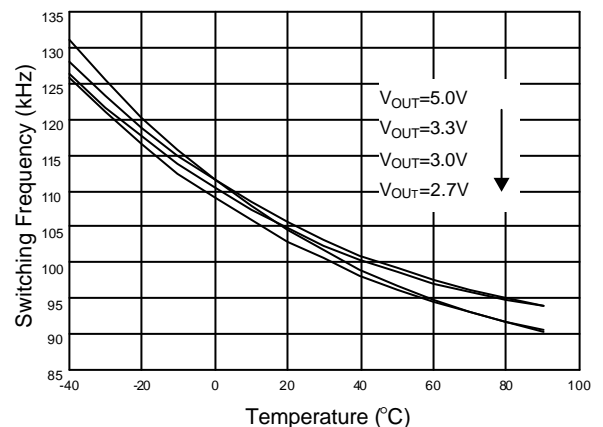


Fig. 14 AIC1640 Switching Frequency vs. Temperature

**TYPICAL PERFORMANCE CHARACTERISTICS (Continued)**

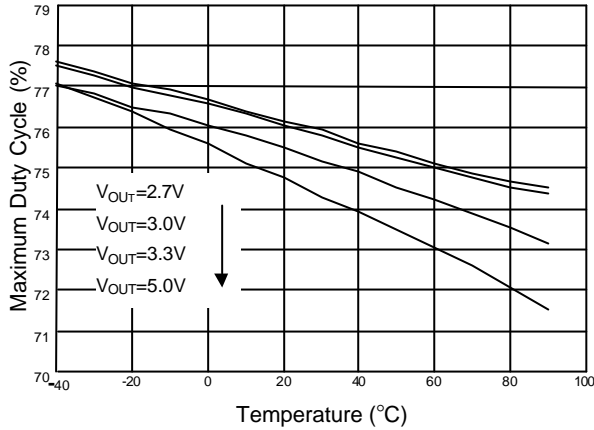


Fig. 15 AIC1640 Maximum Duty Cycle vs. Temperature

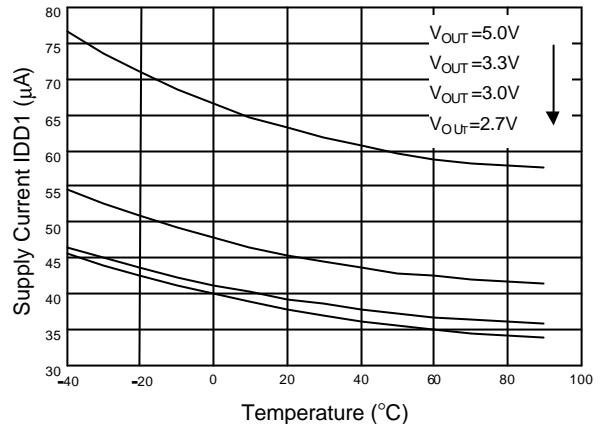


Fig. 16 AIC1640 Supply Current vs. Temperature

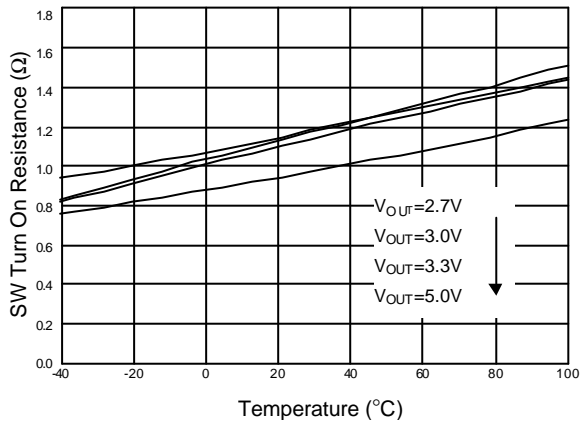
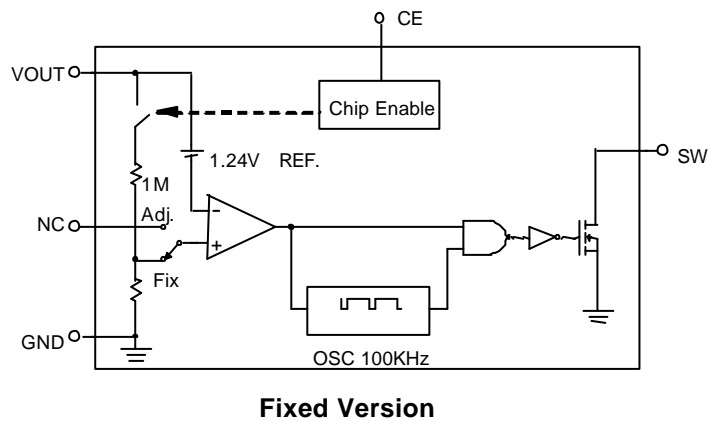
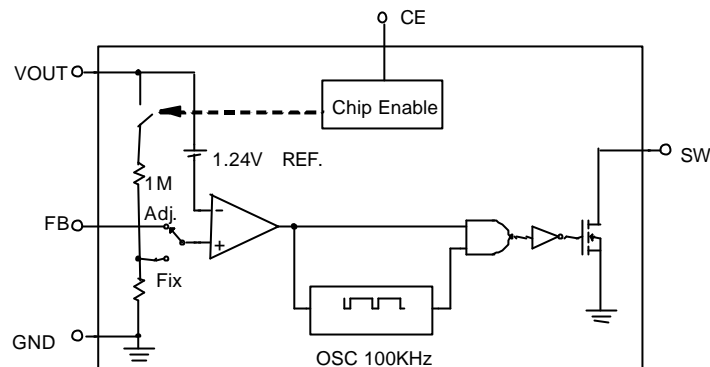


Fig. 17 AIC1640 SW ON Resistance vs. Temperature

**BLOCK DIAGRAM**




**Adjustable Version**

## PIN DESCRIPTIONS

- NC - Not connected.
- VOUT - IC supply pin. Connect VOUT to the controller output.
- CE - Chip enable pin (Active High).
- SW - Internal drain of N-MOSFET switch.

- GND - Ground Terminal. Must be low impedance; solder directly to ground plane.
- FB - Output Voltage Adjustable Pin. VOUT voltage is given by the following equation:

$$\frac{R2}{R1} = \frac{V_{OUT}}{1.24} - 1$$

## APPLICATION INFORMATION

### GENERAL DESCRIPTION

AIC1640 PFM (pulse frequency modulation) converter ICs combine a switch mode controller, N-channel power MOSFET, precision voltage reference, and voltage detector in a single monolithic device. They offer both extreme low quiescent current, high efficiency, and very low gate threshold voltage to ensure start-up with low battery voltage (0.8V typ.). Designed to maximize battery life in portable products, and minimize switching losses by only switching as needed service the load.

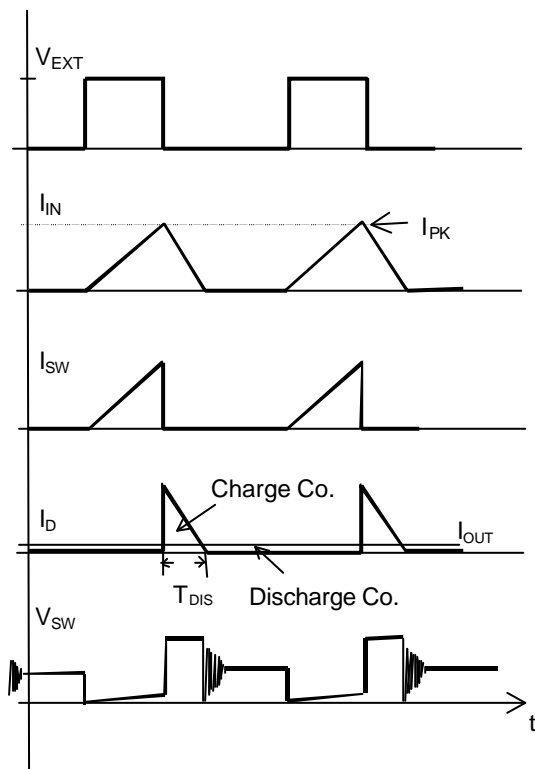
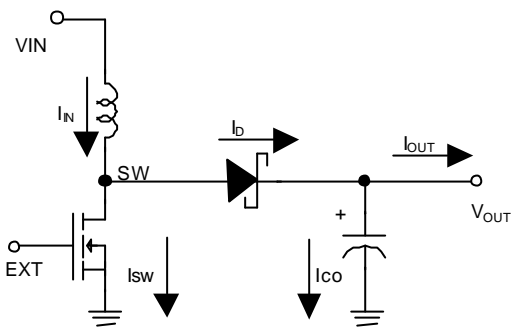
PFM converters transfer a discrete amount of energy per cycle and regulate the output voltage by modulating switching frequency with the constant turn-on time. Switching frequency depends on load,

input voltage, and inductor value, and it can range up to 100kHz. The SW on-resistance is typically 1 to 1.5Ω to minimize switch losses.

When the output voltage drops, the error comparator enable 100kHz oscillator that turns on the MOSFET around 7.5μs and 2.5μs off time. Turning on the MOSFET allows inductor current to ramp up, storing energy in a magnetic field. When MOSFET turns off that forces inductor current through diode to the output capacitor and load. As the stored energy is depleted, the current ramp down until the diode turns off. At this point, inductor may ring due to residual energy and stray capacitance. The output capacitor stores charge when current flowing through the diode is high, and release it when cur-

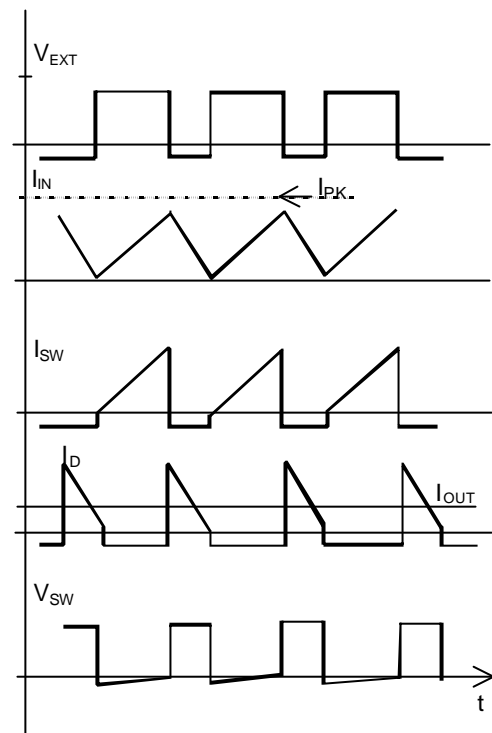
rent is low, thereby maintaining a steady voltage across the load.

As the load increases, the output capacitor discharges faster and the error comparator initiates cycles sooner, increasing the switching frequency. The maximum duty cycle ensure adequate time for energy transfer to output during the second half



**Discontinuous Conduction Mode**

each cycle. Depending on circuit, PFM converter can operate in either discontinuous mode or continuous conduction mode. Continuous conduction mode means that the inductor current does not ramp to zero during each cycle.



**Continuous Conduction Mode**

At the boundary between continuous and discontinuous mode, output current (I<sub>OB</sub>) is determined by

$$I_{OB} = \left( \frac{V_{IN}}{V_{OUT} + V_D} \right) * \frac{1}{2} * \frac{V_{IN}}{L} * T_{ON} * (1 - x)$$

where V<sub>D</sub> is the diode drop,

$$X = (R_{ON} + R_s) * \frac{T_{ON}}{L}$$

R<sub>ON</sub>= Switch turn on resistance, R<sub>s</sub>= Inductor DC resistance

T<sub>ON</sub> = Switch ON time

In the discontinuous mode, the switching frequency ( $F_{sw}$ ) is

$$F_{sw} = \frac{2(L) * (V_{out} + V_D - V_{in}) * (I_{out})}{V_{in}^2 * T_{on}^2} * (1 + x)$$

In the continuous mode, the switching frequency is

$$f_{sw} = \frac{1}{T_{on}} \frac{(V_{out} + V_D - V_{in})}{(V_{out} + V_D - V_{sw})} * [1 + \frac{x}{2} * (\frac{V_{in} - V_{sw}}{V_{out} + V_D - V_{sw}})]$$

$$\cong \frac{1}{T_{on}} \left( \frac{V_{out} + V_D - V_{in}}{V_{out} + V_D - V_{sw}} \right)$$

where  $V_{sw}$  = switch drop and proportion to output current.

### INDUCTOR SELECTION

To operate as an efficient energy transfer element, the inductor must fulfill three requirements. First, the inductance must be low enough for the inductor to store adequate energy under the worst case condition of minimum input voltage and switch ON time. Second, the inductance must also be high enough so maximum current rating of AIC1640 and inductor are not exceeded at the other worst case condition of maximum input voltage and ON time. Lastly, the inductor must have sufficiently low DC resistance so excessive power is not lost as heat in the windings. But unfortunately this is inversely related to physical size.

Minimum and Maximum input voltage, output voltage and output current must be established before and inductor can be selected.

In discontinuous mode operation, at the end of the switch ON time, peak current and energy in the inductor build according to

$$I_{pk} = \left( \frac{V_{in}}{R_{on} + R_s} \right) * \left( 1 - \exp\left(-\frac{R_{on} + R_s}{L} * T_{on}\right) \right)$$

$$\cong \left( \frac{V_{in}}{L} \right) * (T_{on}) * \left( 1 - \frac{x}{2} \right)$$

$$\cong \frac{V_{in}}{L} * T_{on}$$

(simple lossless equation), where

$$x = (R_{on} + R_s) * \frac{T_{on}}{L}$$

$$E_L = \frac{1}{2} L * I_{pk}^2$$

Power required from the inductor per cycle must be equal or greater than

$$\frac{P_L}{f_{sw}} = (V_{out} + V_D - V_{in}) * (I_{out}) * \left( \frac{1}{f_{sw}} \right)$$

in order for the converter to regulate the output.

When loading is over IOB, PFM controller operates in continuous mode. Inductor peak current can be derived from

$$I_{pk} = \left( \frac{V_{out} + V_D - V_{sw}}{V_{in} - V_{sw}} - \frac{x}{2} \right) * I_{out}$$

$$+ \left( \frac{V_{in} - V_{sw}}{2L} \right) * T_{on} * \left( 1 - \frac{x}{2} \right)$$

Valley current ( $I_v$ ) is

$$I_v = \left( \frac{V_{out} + V_D - V_{sw}}{V_{in} - V_{sw}} - \frac{x}{2} \right) * I_{out}$$

$$- \left( \frac{V_{in} - V_{sw}}{2L} \right) * T_{on} * \left( 1 - \frac{x}{2} \right)$$

Table 1 Indicates resistance and height for each coil.

Power Inductor Type		Inductance (mH)	Resistance (W)	Rated Current (A)	Height (mm)
Coilcraft SMT Type (www.coilcraft.com)	DS1608	22	0.10	0.7	2.9
		47	0.18	0.5	
		100	0.38	0.3	
	DO3316	22	0.08	2.7	5.2
		47	0.14	1.8	
Sumida SMT Type CD54		47	0.25	0.7	4.5
		100	0.50	0.5	
Hold SMT Type PM54		47	0.25	0.7	4.5
		100	0.50	0.5	
Hold SMT Type PM75		33	0.11	1.2	5.0

### CAPACITOR SELECTION

A poor choice for a output capacitor can result in poor efficiency and height output ripple. Ordinary aluminum electrolytic, while inexpensive may have unacceptably poor ESR and ESL. There are low ESR aluminum capacitors for switch mode DC-DC converters which work much better than general-purpose unit. Tantalum capacitors provide still better performance at more expensive. OS-CON capacitors have extremely low ESR in a small size. If capacitance is reduced, output ripple will increase.

Most of the input supply is supplied by the input bypass capacitor, the capacitor voltage rating should be at least 1.25 times greater than a maximum input voltage.

### DIODE SELECTION

Speed, forward drop, and leakage current are the three main consideration in selecting a rectifier diode. Best performance is obtained with Schottky rectifier diode such 1N5819. Motorola makes MBR0530 in surface mount. For lower output power

er a 1N4148 can be used although efficiency and start-up voltage will suffer substantially.

### COMPONENT POWER DISSIPATION

Operating in discontinuous mode, power loss in the winding resistance of inductor can be approximate equal to

$$P_{DL} = \frac{2}{3} * \left( \frac{T_{ON}}{L} \right) * (R_s) * \left( \frac{V_{OUT} + V_D}{V_{OUT}} \right) * (P_{OUT})$$

where  $P_{OUT} = V_{OUT} * I_{OUT}$ ;  $R_s =$  Inductor DC R;  
 $V_D =$  Diode drop.

The power dissipated in a switch loss is

$$P_{Dsw} = \frac{2}{3} * \left( \frac{T_{ON}}{L} \right) * (R_{ON}) * \left( \frac{V_{OUT} + V_D - V_{IN}}{V_{OUT}} \right) * (P_{OUT})$$

The power dissipated in rectifier diode is

$$P_{Dd} = \left( \frac{V_D}{V_{OUT}} \right) * (P_{OUT})$$

where  $P_{OUT} = V_{OUT} * I_{OUT}$ ;  $R_s =$  Inductor DC R;